

FIG. 1

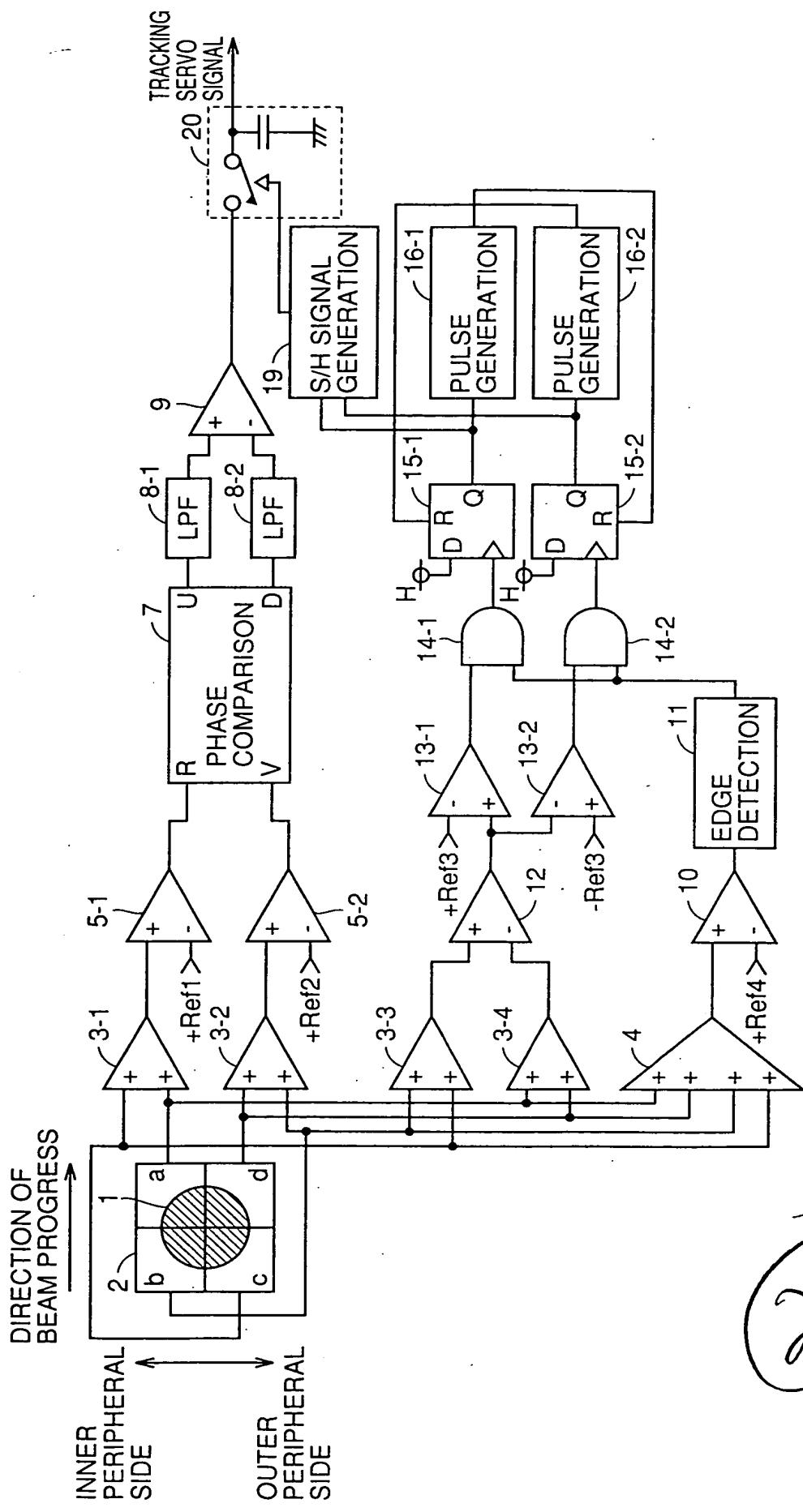


FIG.2

卷之三

- (a) OUTPUT SIGNAL OF ADDITION CIRCUIT 4
- (b) TANGENTIAL PUSH-PULL SIGNAL (OUTPUT SIGNAL OF DIFFERENTIAL CIRCUIT 12)
- (c) OUTPUT OF COMPARING CIRCUIT 10
- (d) OUTPUT OF EDGE DETECTING CIRCUIT 11
- (e) OUTPUT OF COMPARING CIRCUIT 13-1
- (f) OUTPUT OF COMPARING CIRCUIT 13-2
- (g) OUTPUT OF AND GATE (MASK CIRCUIT) 14-1
- (h) OUTPUT OF AND GATE (MASK CIRCUIT) 14-2
- (i) OUTPUT OF FF CIRCUIT 15-1
- (j) OUTPUT OF PULSE GENERATING CIRCUIT 16-1
- (k) OUTPUT OF FF CIRCUIT 15-2
- (l) OUTPUT OF PULSE GENERATING CIRCUIT 16-2
- (m) OUTPUT OF S/H SIGNAL GENERATING CIRCUIT 19

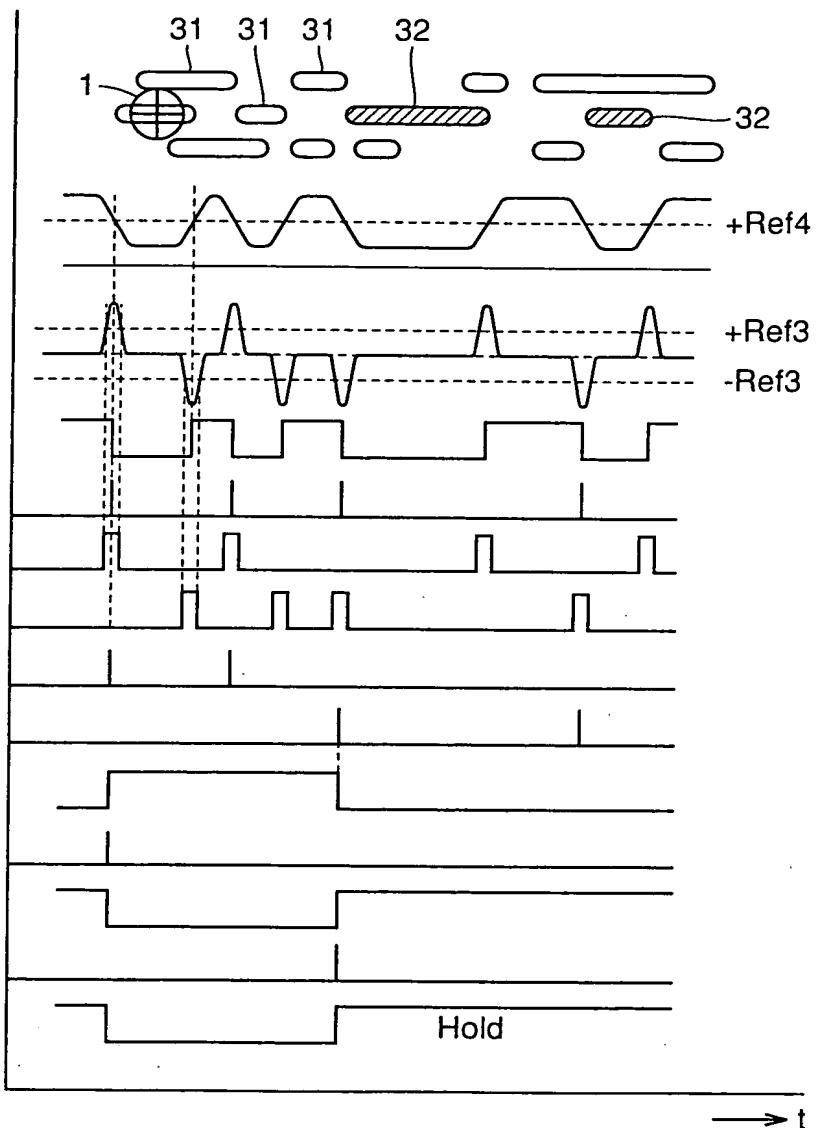


FIG.3

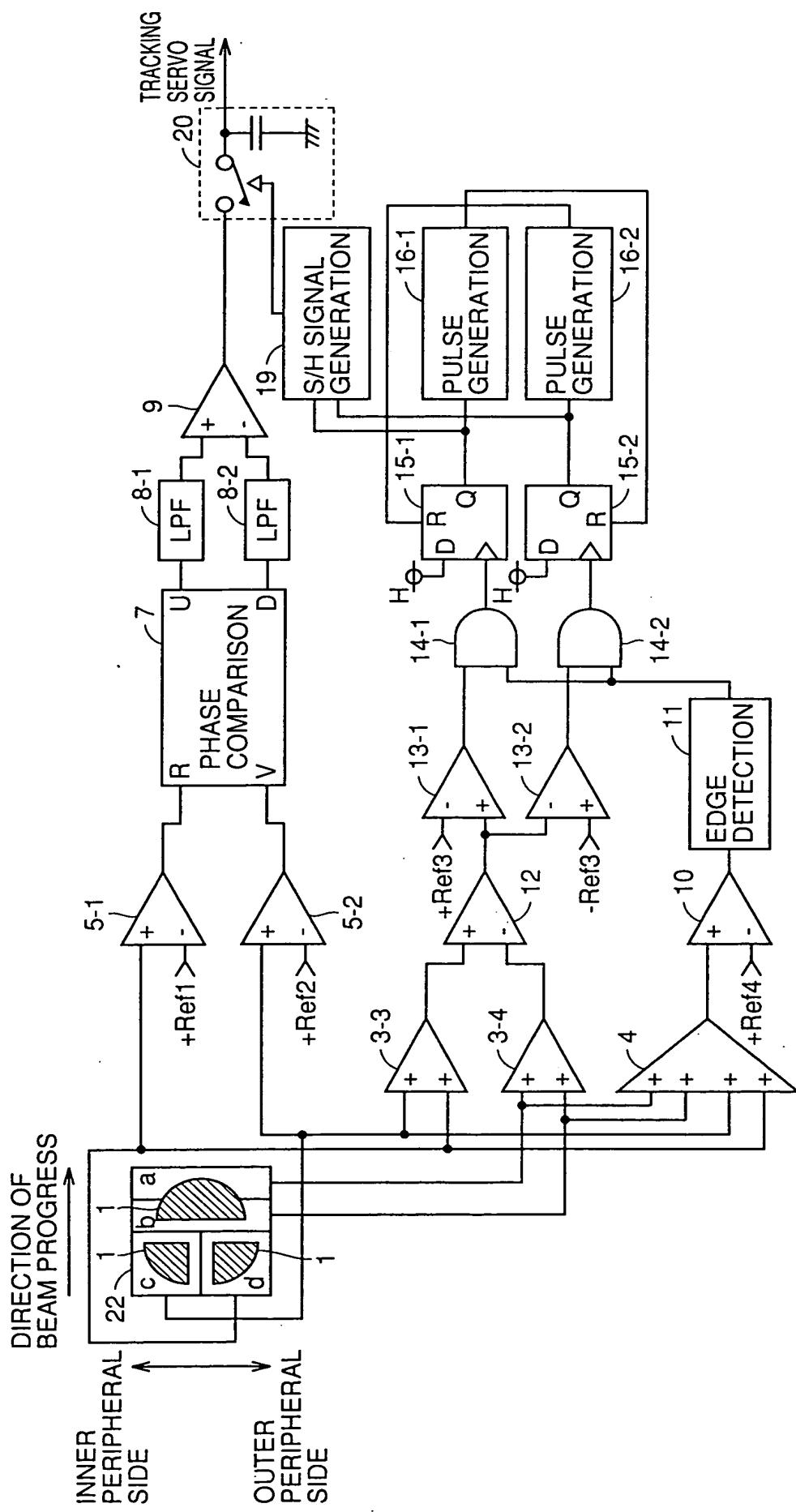
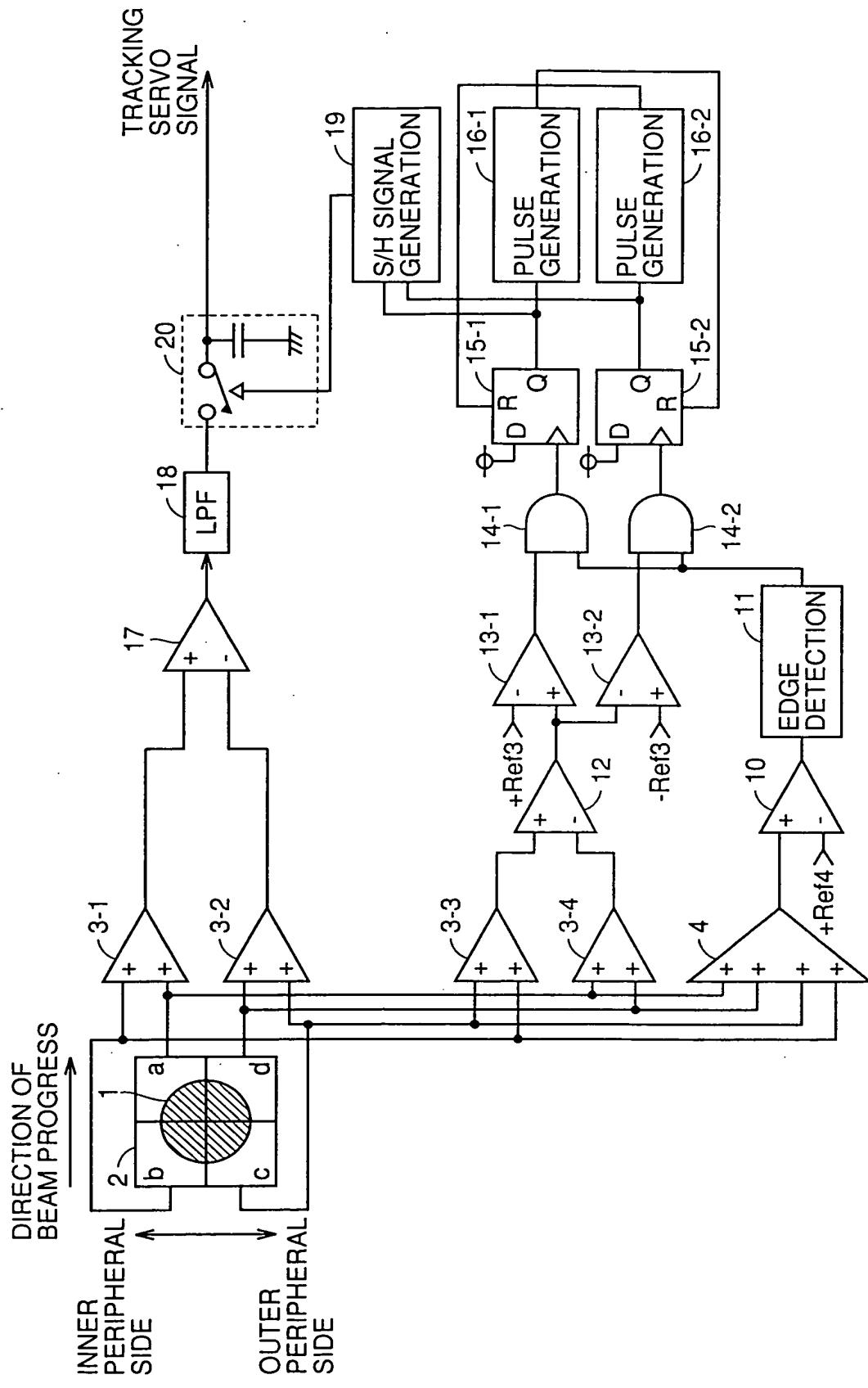


FIG. 4



F05060 "200000000

FIG.5

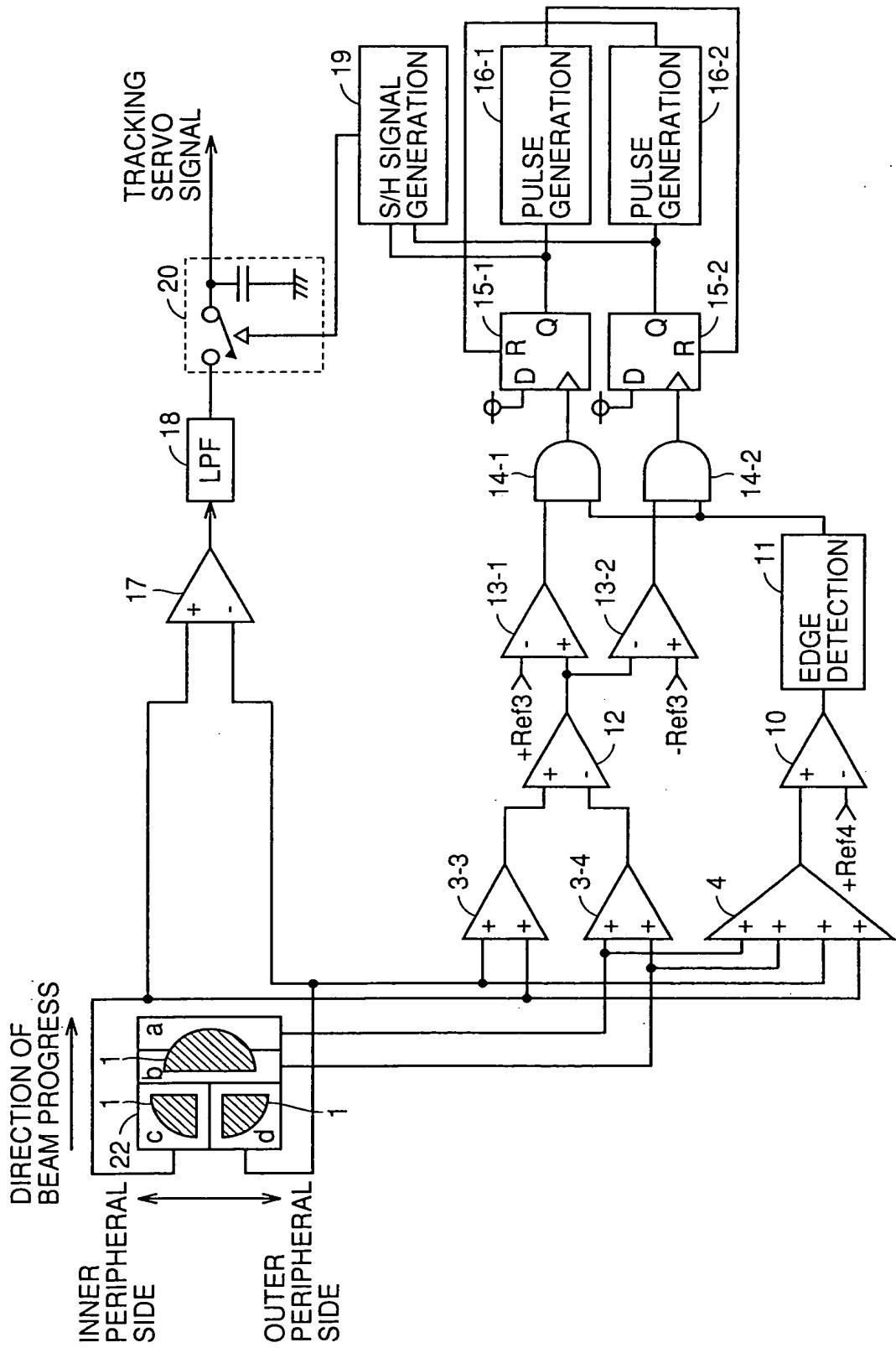


FIG. 6

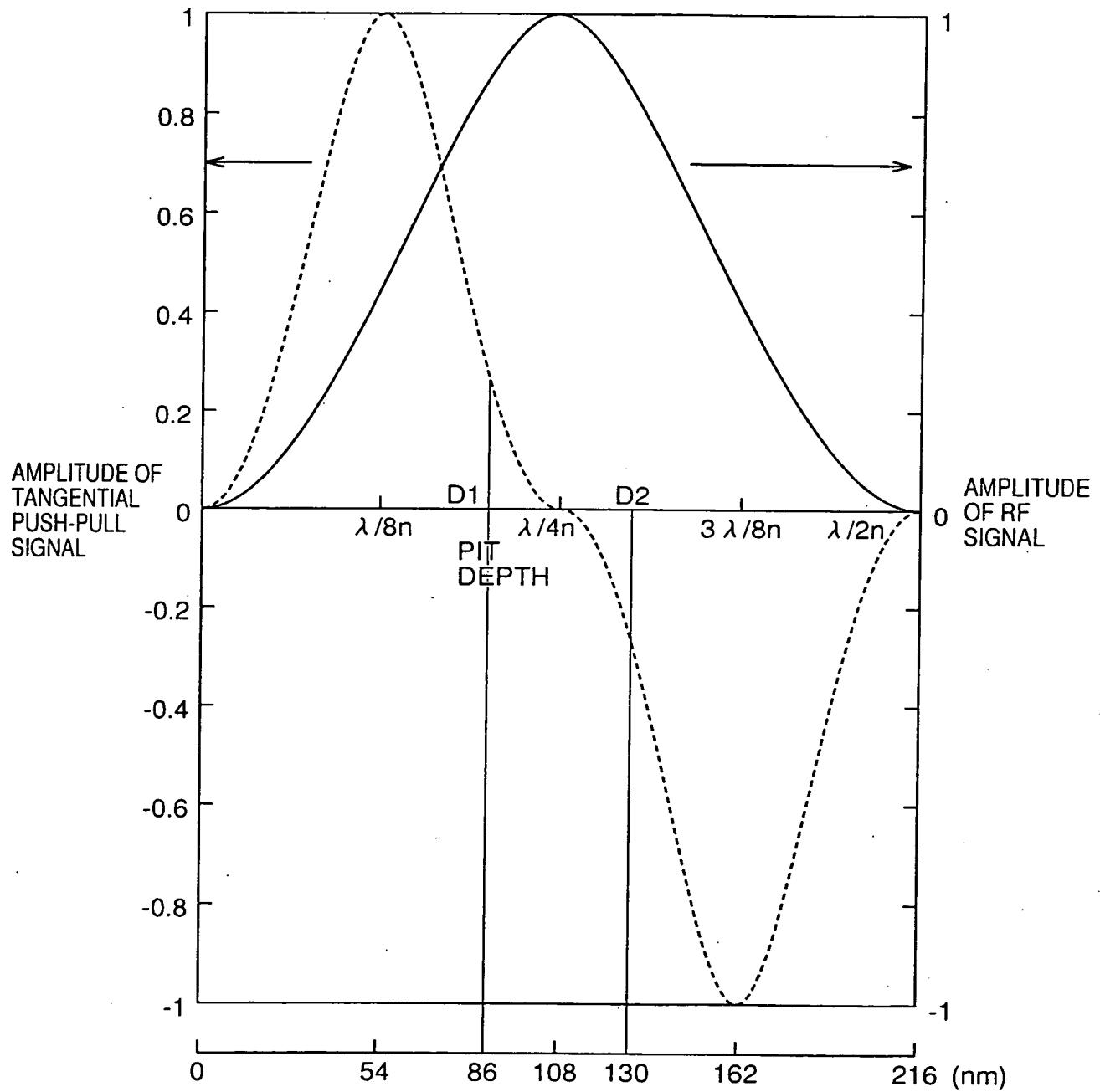
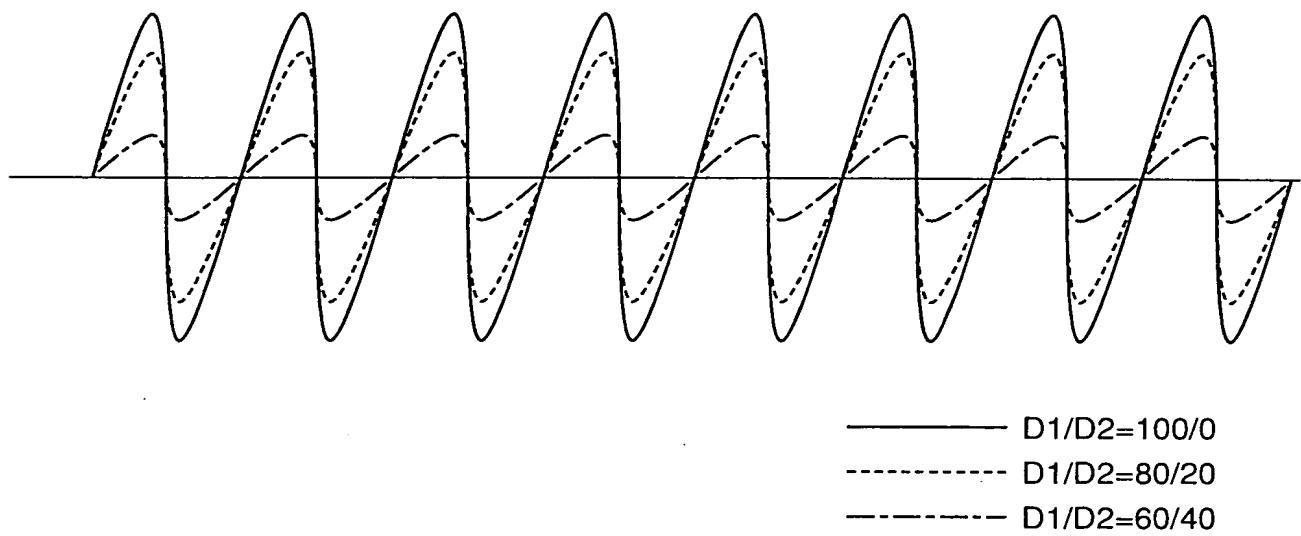


FIG.7



F050E0 "28000260

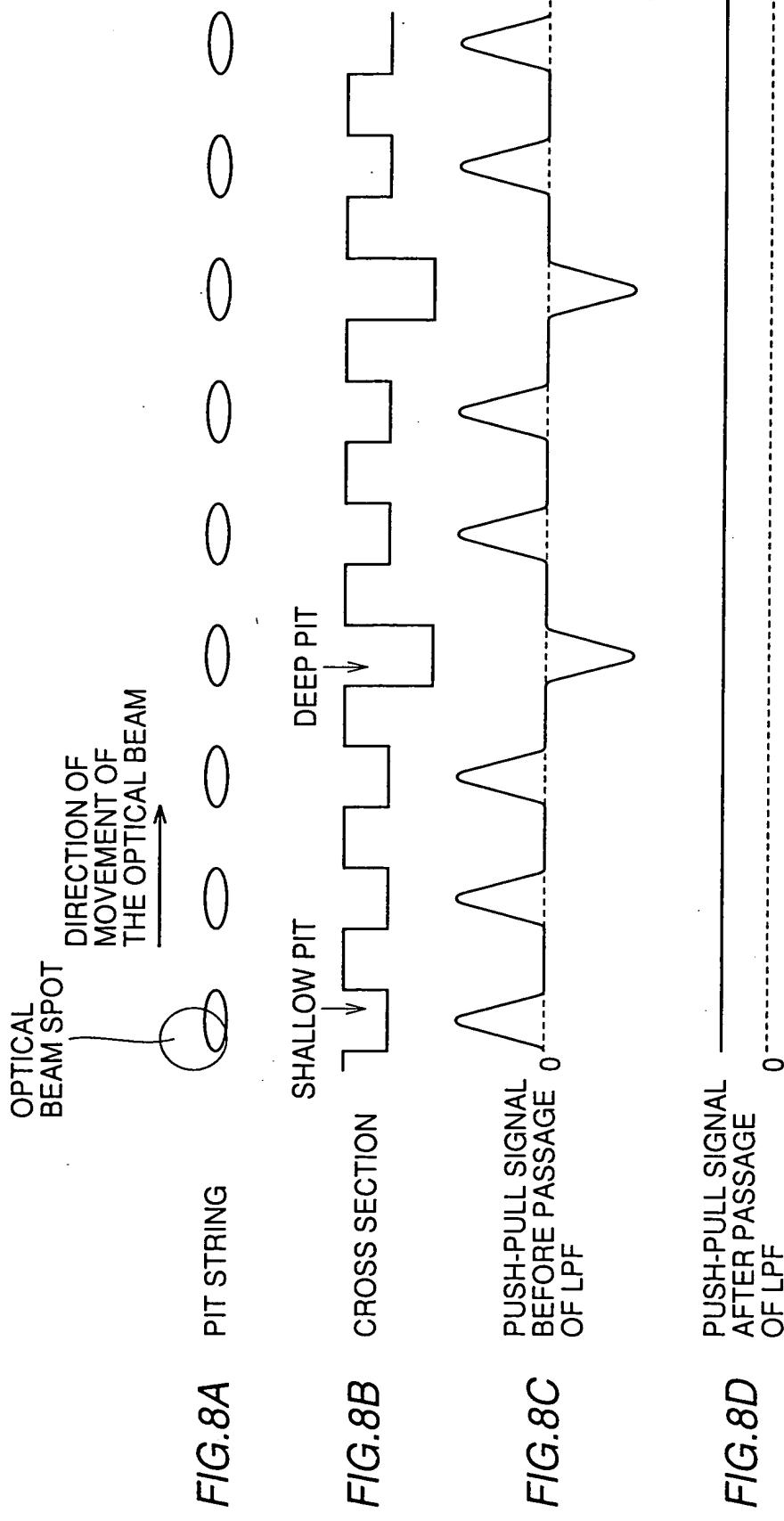
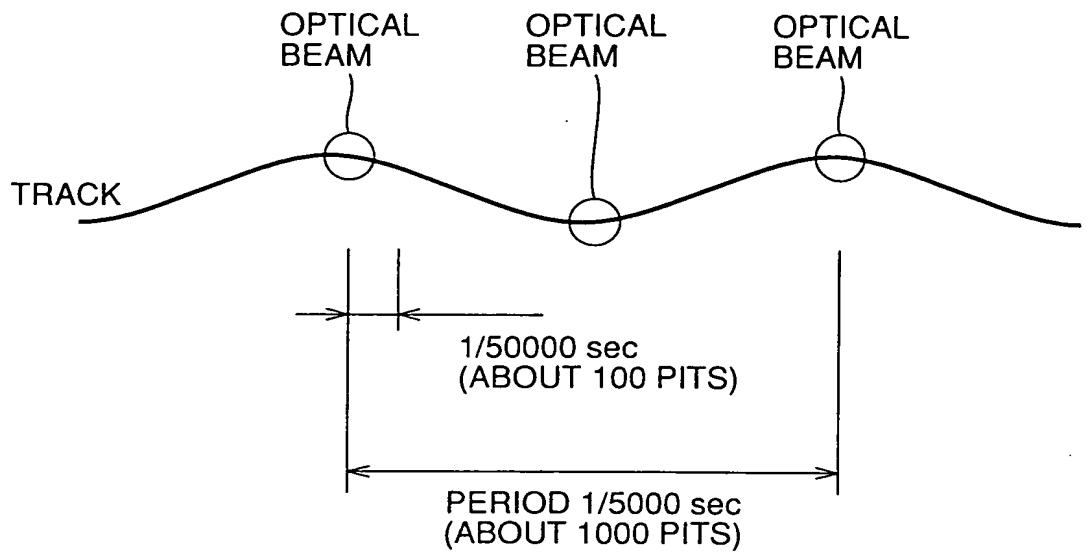


FIG. 9



□ 900000000

FIG. 10

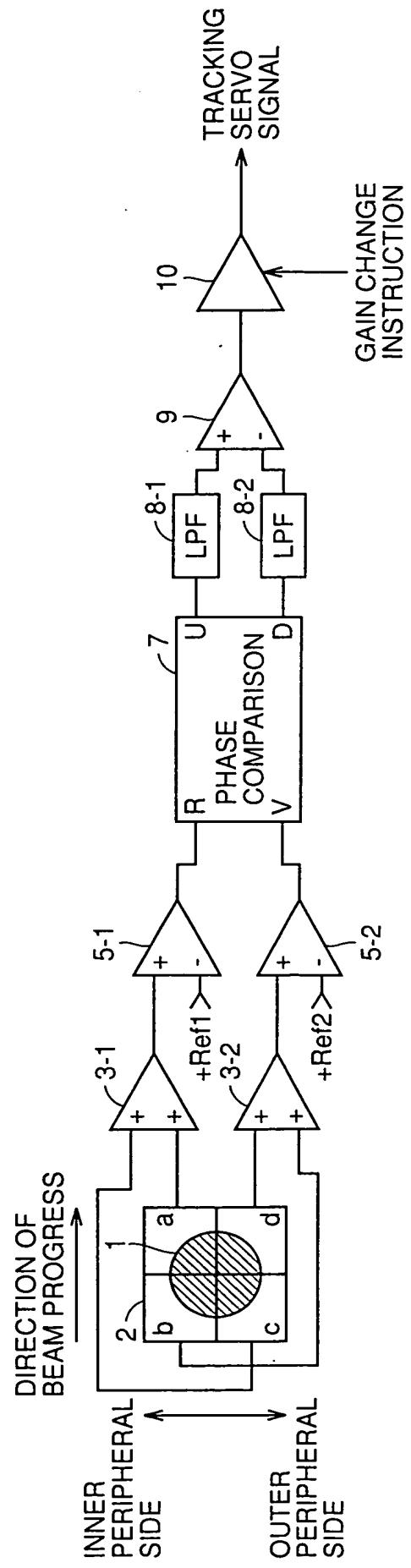


FIG. 11

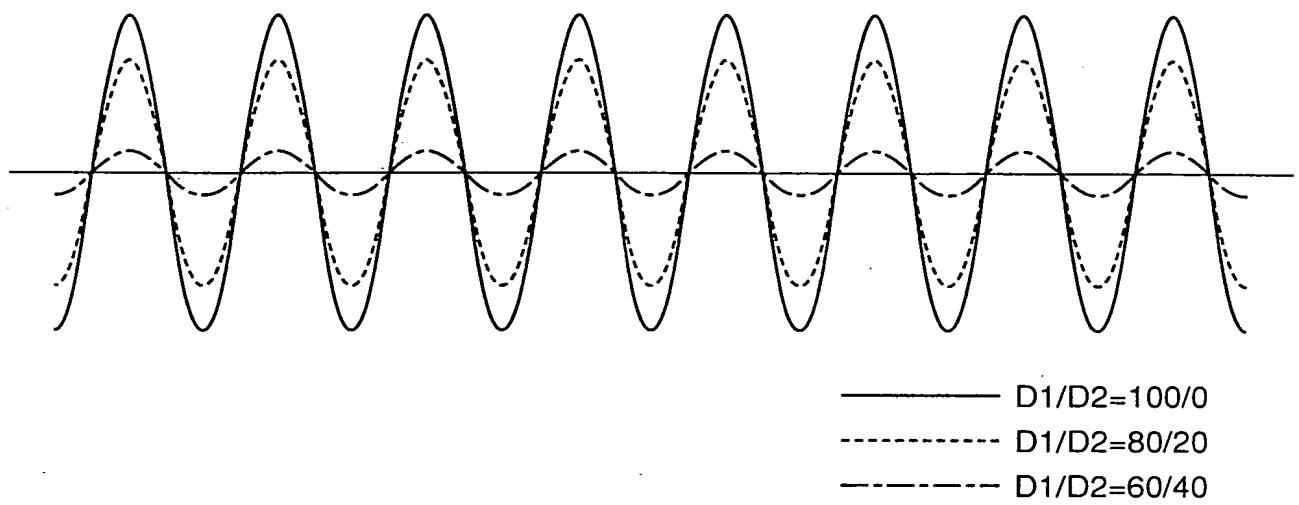
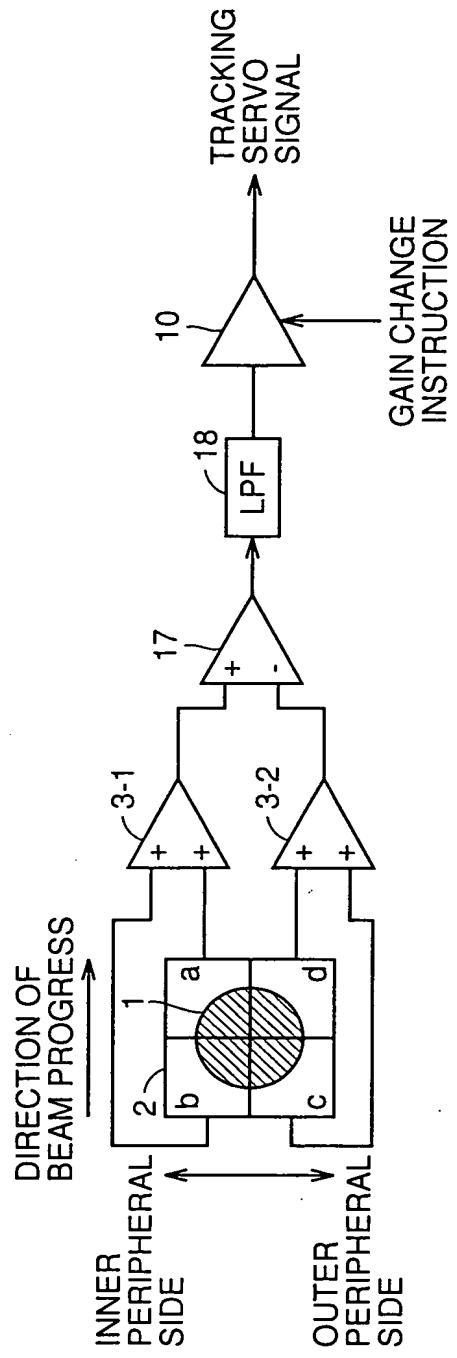


FIG. 12 " 2800 DE 60

FIG. 12



195060 23000360

FIG. 13A MARK AND  
PIT STRING

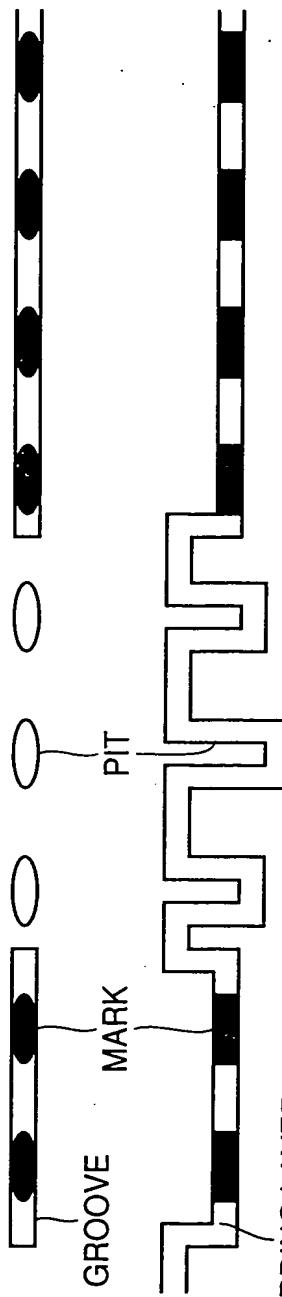


FIG. 13B CROSS  
SECTION

RECORDING LAYER

7050E0 230000360

FIG.14A GROOVE AND  
PIT STRING

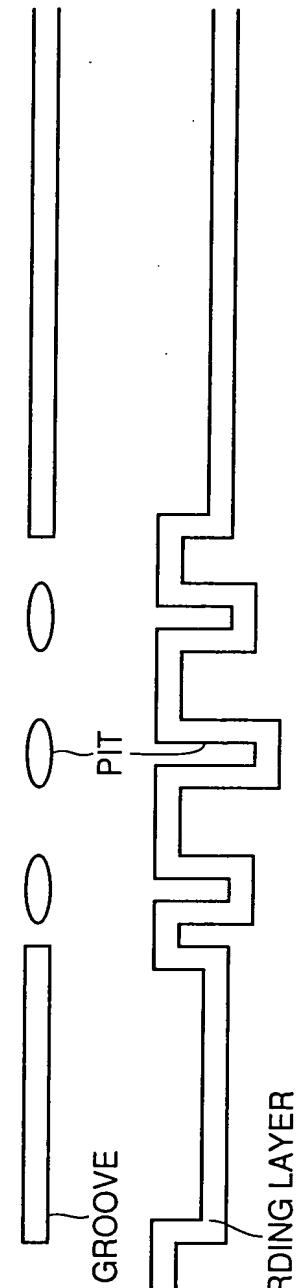
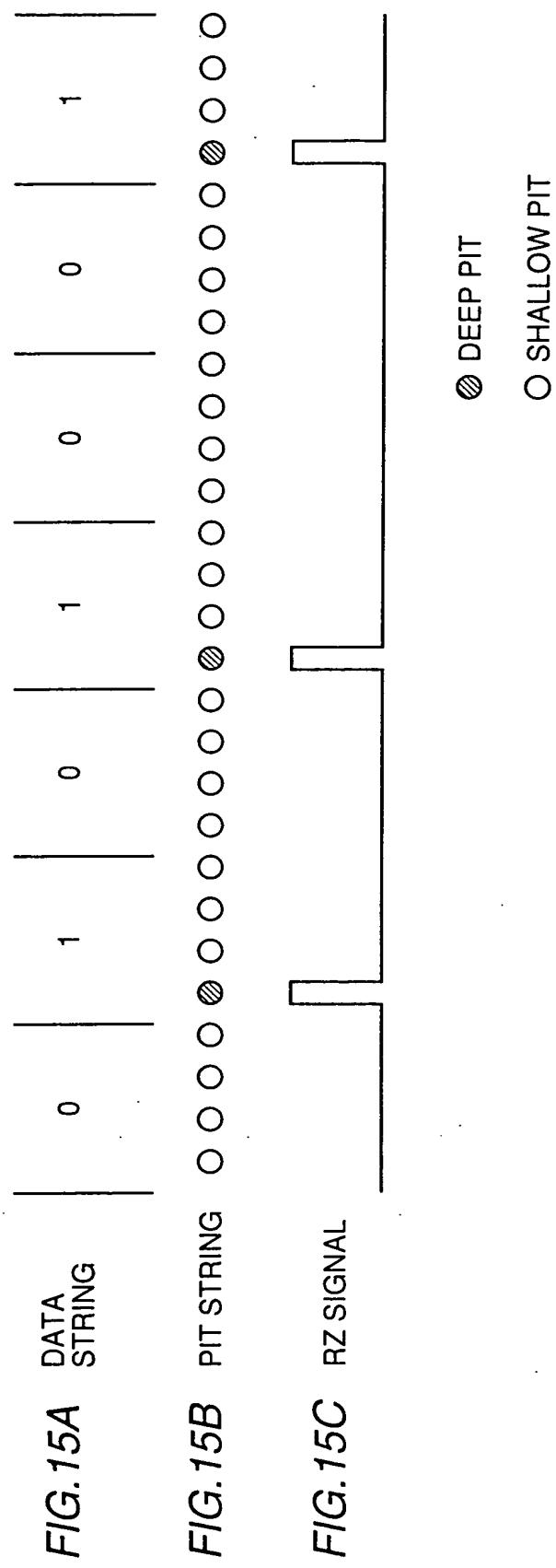


FIG.14B CROSS  
SECTION  
RECORDING LAYER

09300922 030501



TO 50 E0 " 2300000000

FIG. 16 PRIOR ART

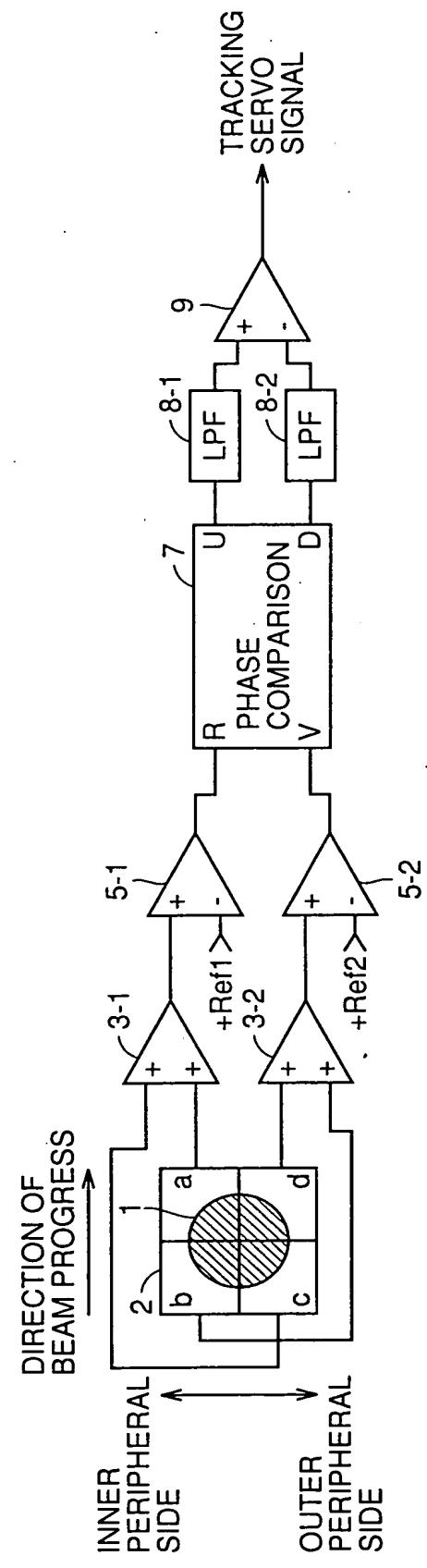


FIG.17 PRIOR ART

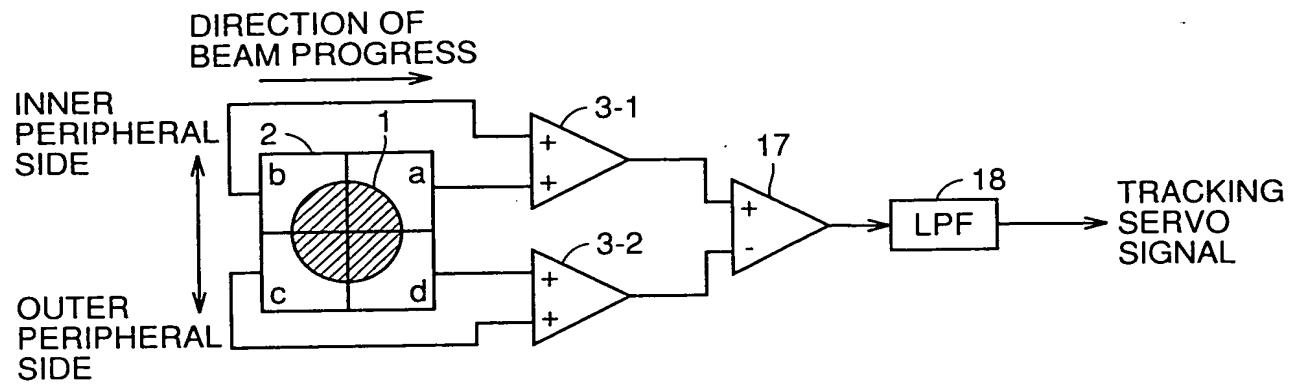


FIG.18

